



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yuesong He; Kent Kuohua Chang; and R Lee Tan
Assignee: Advanced Micro Devices, Inc.
Title: Method and Apparatus for Improved Performance of Flash Memory Cell Devices
Serial No.: 09/492,243 Filing Date: January 27, 2000
Examiner: E. Ortiz Group Art Unit: 2815
Docket No.: M-7469 US

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Payton
FEB 23 2002
TC 2000 MAIL ROOM

Newport Beach, California
February 11, 2002

Box Non-Fee Amendment
Attn: Official Draftsperson
COMMISSIONER FOR PATENTS
Washington, D.C. 20231

REQUEST TO AMEND DRAWINGS
UNDER 37 C.F.R. § 1.121

Sir:

Pursuant to 37 C.F.R. §1.121, Applicants respectfully requests permission to amend Figure 2 as shown in red on the accompanying sheet. Element numbers 58s, 58 m1 through m3, 44s, 44 m1 through m3, 54 m1 through m3, and 56 m1 through m3 have been amended to be consistent with the specification as set forth on page 5, line 4 through page 6, line 14. As filed, Figure 2 did not differentiate these element numbers with the letters denoting which transistor/memory cell was involved (specifically select transistor, memory cell WL0, memory cell WL1, and memory cell WL2). No new matter has been added. In addition, the drawing margins have been changed responsive to the "Notice of Draftsperson Patent Drawing Review" mailed 9/11/01.

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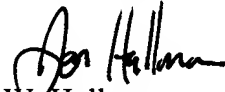
Applicants believe that no fee is due. However, the Commissioner is hereby authorized to charge any necessary fees or credit any overpayments to Deposit Account No 19-2386.

Should the Examiner have any questions concerning this request, the Examiner is invited to call the undersigned at (949) 718-5200.

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Respectfully submitted,



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